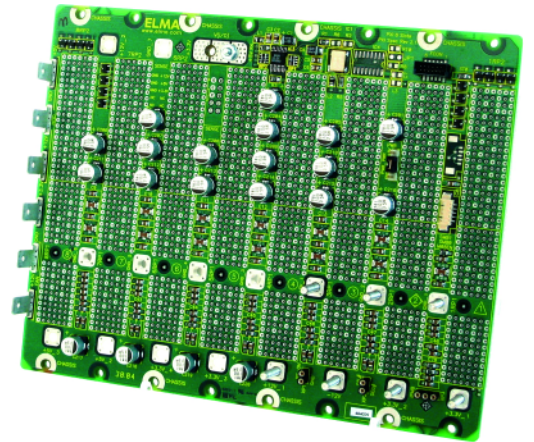


PXI 3U

FEATURES

- Compliant with
 - PICMG 2.0 R3.0 CPCI Core Specification
 - PXI SA R2.1
 - PICMG 2.1 R2.0 Hot Swap Specification
 - PICMG 2.9 R1.0 System Management Specification
- System slot left
- Star trigger on slot 2
- Modular power system:
 - ATX cable
 - M3 power bolts (cable lugs, washer and nuts enclosed)
 - Positronic PCI H47 (pass through contacts enclosed)
 - DIN 41612 type M (pass through contacts enclosed)
- 10-layer construction
- Number of slots: 8
- Virtually zero crosstalk
- Fastons for supplying HDD and fans
- Decoupling of the DC voltages far beyond 1GHz (act as EMI-filter)
- Hot swap capability
- PCB height 128.7 mm
- PCB thickness 3.5 mm
- PCB width 161.56 mm
- Connector for status signals "FCON"
 - Part number of mating connector including 1.0 m cable, open end: 008-083



ORDER CODE

859 3 2 - 0 - 6 0 0

option	code and description	note
System Slot	3 - left side	
Clock frequency	0 - 33 MHz, V(I/O) +5 V	
Slots	08	
Bridge	6 - without bridge	
Power connection	8 - Power bolts 9 - DIN 41612 Type M connector H - ATX cable K - Postronic PCI H47	
Bus width	0 - 64 bit	
Contact plating	0 - Power inputs tinned IEC pins gold-plated class 2	

GENERAL

About PXI

The PCI eXtensions for Instrumentation (PXI) specification defines a rugged PC-based platform for measurement and automation systems. It combines the high-speed PCI bus with integrated timing and triggering designed specifically for measurement and automation applications to deliver significant performance improvements over older architectures.

By using the de facto standard PCI bus, PXI modular instrumentation systems can benefit from widely available software and hardware components. The software applications and operating systems that run on PXI systems are already familiar to users because they are already in use on common desktop computers. PXI meets your needs by adding rugged industrial packaging, plentiful slots for I/O, and features that provide advanced timing and triggering capabilities.

Special PXI features

A System Reference Clock (10 MHz TTL clock) provides a built-in common reference clock for synchronization of multiple modules in a measurement or control system.

PXI Trigger Bus: Eight trigger bus lines for synchronization and communication between modules.

Star Trigger Bus has an independent trigger line for each slot oriented in a star configuration from a special star trigger slot (slot 2 in any PXI chassis). This feature addresses high-speed synchronization where you can distribute start/stop trigger signals from the master measurement module in the star trigger slot with low delay and skew.

The PXI Local Bus is a daisy-chained bus that connects each peripheral slot with its adjacent peripheral slots to the left and right.

PAPRAMETERS

CLIMATIC

- Operating temperature -40 °C up to +85 °C
- Storage temperature -55 °C up to +85 °C
- Climatic conditions category to IEC 68/1: 25/085/21

MECHANICAL

- Flammability:
 - PCB: UL 94 V-0
 - Connectors: UL 94 V-0/-1
- Vibration:
 - According to DIN 41640 part 15: 10 Hz to 500 Hz 5 g rms
 - Impact (10 impacts per axis x,y,z) 50 g, 6 ms.
- Layerstackup 10 layers
- Connector: 2 mm pitch, 7 rows,
 - Quality class 2 compliant to spec. IEC 61076-4-101 and BELLCORE GR-1217-CORE
 - Insertion force 0.75 N and extraction force 0.15 N of every contact

ELECTRICAL

- According to PICMG 2.0 R.3.0 and PXI SA R2.1
- VI/O configurable to +3.3 V or +5 V (+5 V factory settings)
- Clock frequency: 33 MHz
- Bus width: 64 bit
- Data transfer rate: max. 533 Mbyte/s (66 MHz/64 bit)
- Impedance Z0 of cPCI signals without connectors and daughter cards: 65 Ohm +/-10 %
- Trigger bus terminated with Schottky Diode Array
- Current carrying capacity of power planes
 - +3.3 V/GND: 10 A/slot
 - +5 V/GND: 8 A/slot
- Max. voltage drop (center to boardout): 20 mV

AVAILABLE SLOTS

No. of slot 8

PCB width (mm) 161.56